BNL TASK LIST

- 1. FPD
 - a. make changes from FPD++ to FMS
 - b. install new racks west-south, west-north, tunnel
 - i. obtain 2 9u VME crates
 - ii. convert P3 backplanes
 - iii. install cooling
 - iv. Dress FPW cabling in back of crates
 - c. Install QT boards and test
 - d. Install 2nd concentrator in FMS racks
 - e. Bring FPE back up to full capacity
- 2. BEMC
 - a. check arrival time at DSM of bits from differ crates in BEMC (Oleg.)
- 3. TCD
 - a. Do we need a second crate for cable driver cards?
- 4. BBC
 - a. Fab and install cable clamps for TAC boards (Ridgeway)
- 5. Scaler
 - a. Investigate problem with VME scalers
 - i. Move all level converters to PMD crate in 1A2
 - b. New scaler distribution system
 - i. Design fpga based system
 - ii. Fab and test new fpga-based system
 - iii. Install on platform and cable
 - c. 48bit scaler
 - i. what remains to be done?
- 6. TOF
 - a. Install proto-type TOF-DSMI
- 7. STP
 - a. What tests remain for STP?
- 8. Platform (general)
 - a. Get broken fibers repaired
 - b. Swap out noisy DSM in CTB slot 0x18 and fix cblt in 0x1f
 - c. Get myrinet monitors hooked up to Ethernet and configured
 - d. Move all cpu's and servers to trg.bnl.local network
 - e. Make L1/L0 connection to 2nd concentrator(?)
- 9. L2 work
 - a. Implement 2nd concentrator
 - b. Implement DDL card into L2
- 10. Counting house (general)
- 11. general
 - a. repair and test DSMs, CDBs, DIBs,
 - b. move startrg2 -> startrg
 - c. make new linux-based startrg2

12. documentation

- a. post task list on web (run2007)
- b. post fiber document
 - i. get map overall star fiber map from dan
 - ii. finish fiber owners and use document
 - iii. create web table with info